

REJECTIONS UNDER 35 U.S.C. 102(e)

Roy does not disclose “a processor interface unit; and a cache to store information received from a processor coupled to the processor interface unit, the cache to store disposable information” as claimed in claim 1. Roy discloses a processor, rather than a processor interface unit to receive information from a processor. Also, the cache disclosed by Roy resides as part of the processor itself, rather than as part of a separate apparatus coupled to a processor via a processor interface unit.

Further, Applicant’s specification defines “disposable information” as information that is meant to be read by a system component only once. That is, “disposable information” does not need to be retained by the computer system once the information has been consumed by a system component. Once a particular cache entry containing disposable information is read by a system component, that cache entry may be overwritten. This differs from Roy in that “non-disposable” information is defined as information that will not be overwritten. The Examiner mentions that if information is not “non-disposable”, then it may be overwritten.

The comments above in connection with claim 1 are also applicable to independent claims 11 and 23. For these reasons, claims 1-26 are not anticipated by Roy.

CONCLUSION:

In view of the foregoing, Applicant submits that claims 1-26 are distinguished over the cited art and are in condition for allowance. Allowance of claims 1-20 is respectfully requested.

DEPOSIT ACCOUNT AUTHORIZATION

Authorization is hereby given to charge our Deposit Account No. 02-2666 for any charges that may be due.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN

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